

A V-Band Four-Channel Beamforming Receiver IC for Very Low Earth Orbit Satellite Communication Systems

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Abstract—This paper presents a V-band four-channel beamforming receiver for very low Earth orbit satellite communication systems. Implemented in a $0.13\ \mu\text{m}$ SiGe BiCMOS process, the IC integrates low-noise amplifiers, variable-gain phase shifters, and a 4-to-1 power combiner together with serial peripheral interface control logic and bias circuitry within a $2.0 \times 1.3\ \text{mm}^2$ core. Except for the LNA input stages, the signal path is fully differential, and an on-chip RF balun converts the output to a single-ended signal. The simulated performance of the receiver shows a peak gain of 21 dB and a 3 dB bandwidth of 45–55 GHz (20% fractional), with a coherent noise figure of 2.7 dB. The phase sweep from 0° to 360° in 5.625° steps yields an RMS amplitude error of 0.21 dB and an RMS phase error of 1.3° .

Index Terms—RF receiver, 6G, satellite communication, low-noise amplifier, front-end, phased array, phase shifter, beamformer.

I. INTRODUCTION

Wireless communication has advanced through successive generations and is now transitioning to the sixth generation (6G). Unlike previous systems that relied solely on terrestrial base stations and user equipment, 6G is expected to incorporate low Earth orbit (LEO) satellite communication (SATCOM) to provide truly global coverage. In addition, Q/V-band links, which offer wider bandwidth and thus higher potential data rates than conventional K/Ka-band links, are anticipated to play a key role in very low-Earth orbit (VLEO) platforms [1]–[5].

This work presents a V-band four-channel beamforming receiver for VLEO SATCOM systems. The receiver operates over 45–55 GHz and integrates low-noise amplifiers (LNAs), variable-gain phase shifters, and a power combiner, together with an on-chip serial peripheral interface control and bias circuitry.

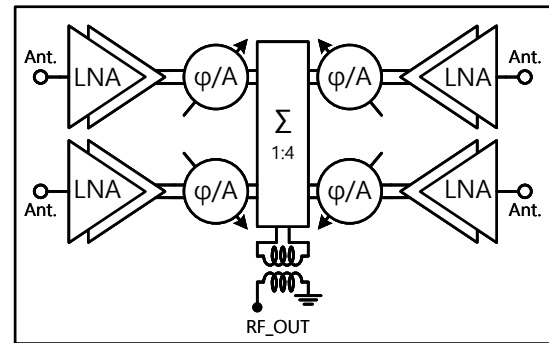


Fig. 1. Schematic diagram of the proposed V-band 4-channel beamforming receiver.

II. V-BAND FOUR-CHANNEL BEAMFORMING RECEIVER IC

Fig. 1 shows the schematic diagram of the proposed four-channel beamforming receiver. Apart from the input stage of each LNA, the entire signal path operates in fully differential mode. A variable-gain phase shifter is employed to reduce the overall die area. The integrated phase shifter employs an active architecture and supports a 6-bit, 5.625° resolution.

To minimize the footprint of the layout, a single 4-to-1 power combiner is adopted in place of three 2-to-1 combiners. The differential output of the combiner is converted to a single-ended signal by an on-chip RF balun.

The proposed beamforming receiver IC is designed in a $0.13\ \mu\text{m}$ SiGe BiCMOS process. The layout of the chip is shown in Fig. 2. The core occupies $2.0 \times 1.3\ \text{mm}^2$, and the

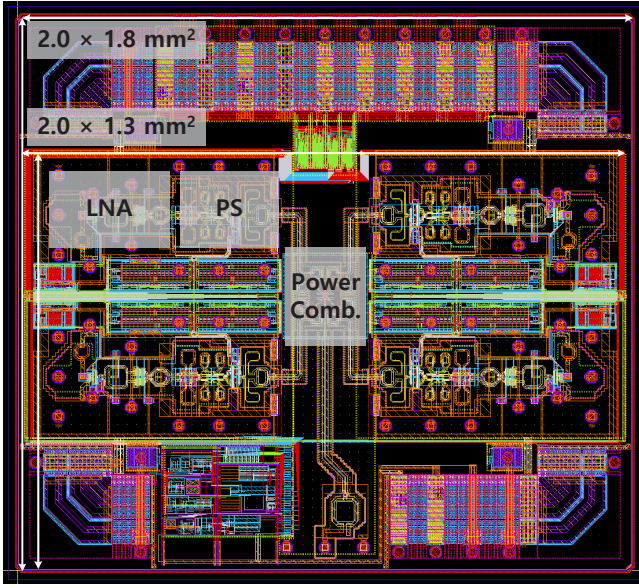


Fig. 2. Chip layout of the proposed four-channel beamforming IC.

overall die size, including pads, is $2.0 \times 1.8 \text{ mm}^2$.

III. PERFORMANCE OF THE BEAMFORMING RECEIVER

Fig. 3(a) presents the simulated gain and noise-figure performance of the proposed beamforming receiver when coherent inputs are applied to all four channels. The receiver delivers a peak gain of 21 dB and maintains a 3 dB bandwidth from 45 to 55 GHz, corresponding to a fractional bandwidth of 20%. Its coherent noise figure is simulated to be approximately 2.7 dB in the band.

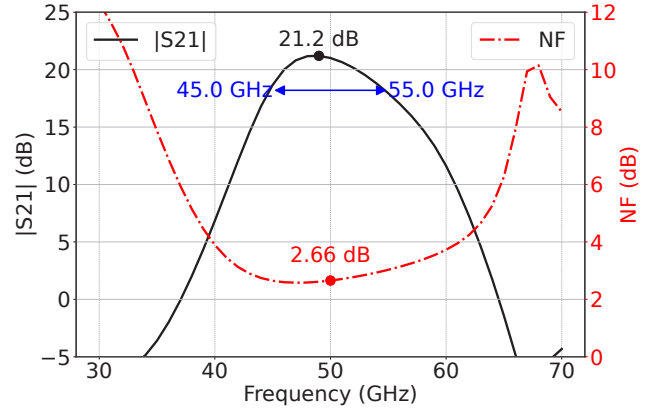
Fig. 3(b) illustrates the simulated phase-shifting performance of the beamformer. When the phase control is swept from 0° to 360° in 5.625° steps, the root-mean-square (RMS) amplitude error remains below 0.21 dB, whereas the RMS phase error stays within 1.3° . Nominal power consumption of the receiver is 0.29 W with 2.4 V supply voltage.

IV. CONCLUSION

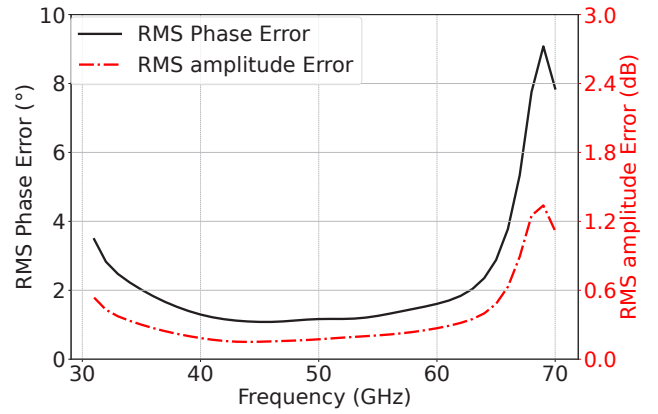
A V-band, four-channel beamforming receiver for VLEO SATCOM systems is presented. Implemented in a $0.13 \mu\text{m}$ SiGe BiCMOS process, the receiver achieved a small-signal coherent gain of 21 dB and a coherent noise figure of 2.7 dB in simulation. The proposed IC could be considered a viable candidate for incorporation into low-noise VLEO phased-array receiver systems.

V. ACKNOWLEDGEMENT

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(a)



(b)

Fig. 3. Simulated RF performance of the Beamformer: (a) Coherent gain and noise figure versus frequency and (b) RMS amplitude and phase errors for phase control over $0^\circ - 360^\circ$ in 5.625° steps.

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